



PR/158729 | Senior Engineering Manager

募集職種

人材紹介会社

ジェイエイシーリクルートメントマレーシア

求人ID

1527079

業種

その他（メーカー）

雇用形態

正社員

勤務地

マレーシア

給与

経験考慮の上、応相談

更新日

2025年04月01日 14:01

応募必要条件

職務経験

3年以上

キャリアレベル

中途経験者レベル

英語レベル

ビジネス会話レベル

日本語レベル

ビジネス会話レベル

最終学歴

短大卒：準学士号

現在のビザ

日本での就労許可は必要ありません

募集要項

Senior Engineering Manager

Market leader for high-power AC to DC converters and is currently looking for Senior Engineering Manager based in Penang Malaysia.

Job Descriptions:

- Responsible for leading continuous products margin enhancement through monthly yield review to closely monitor key indicators and drive action needed with Product and Test Engineers.
- Regular interface with internal Test, Quality, Package, as well as subcons and steer appropriate prioritization of our efforts.
- Instill robust discipline and formalized best practices within Asia Operations that will ensure we make deliver commitments without compromising yield and quality.
- Responsible for both Wafer Sort and Final Test operations and collaborate with HQ including Technology group to align on test method, limit setting, debug, FA and discrepant material disposition.
- Autonomously qualify new product derivatives through over temperature characterization. Work with various engineering groups at HQ to debug product/process/package issues.

Job Requirements:

- Experienced in a high-volume production environment.
- Proven track record of problem solving and driving the resolution of poor yield and/or quality due to assembly, test and fab processes.
- Proficient in statistical data analysis and able to guide their team on issue segmentation principles.
- Experience in software like Spotfire and JMP.
- Flexible to travel as needed to several Asian factory locations (20% to 50%)
- At least 5 years of Management experience overseeing an engineering team supporting Semiconductor Product Engineering and or Assy/Test.
- More than 10 years of engineering experience as a Product Engineer.
- Knowledge of and ability to support automated test equipment, wafer sort and final testing
- Experience with CMOS circuit basics and device physics including BiCMOS is a plus.
- Experience with high voltage, high power testing is a plus.
- Bench equipment setups and bench evaluation of semiconductors experience is desired.

会社説明