



【1000～1200万円】 Principal Engineer Lead/Member of Technical Staff ...

詳細ご説明致しますので、お問い合わせください。

募集職種

人材紹介会社

株式会社ジェイ エイ シー リクルートメント

採用企業名

社名非公開

求人ID

1516811

業種

電気・電子・半導体

会社の種類

外資系企業

雇用形態

正社員

勤務地

広島県

給与

1000万円～1200万円

勤務時間

08:30～17:15

休日・休暇

【有給休暇】有給休暇は入社後7ヶ月目から付与されます 入社7ヶ月目には最低10日以上 【休日】完全週休二日制 年末年始 有給休...

更新日

2025年04月04日 02:00

応募必要条件

キャリアレベル

中途経験者レベル

英語レベル

ビジネス会話レベル

日本語レベル

ネイティブ

最終学歴

高等学校卒

現在のビザ

日本での就労許可が必要です

募集要項

【求人No NJB2238655】

As a Diffusion Principal Engineer lead or Member of Technical Staff in the Process Development (PD) group you will be expected to develop Diffusion process with latest deployment of technology nodes the process margin improvement work necessitated thereof as well as to ensure extended engagement with manufacturing on the upcoming technology nodes. Expected to work independently with minimal direction and complete projects in a timely manner and achieve set objectives.

■Responsibilities will include but are not limited to:

- Support Diffusion Process developments/improvements as well as technology transfer on DRAM in R D at initial stages and carry on subsequently in the manufacturing environment.
- Identify and evaluate interactions between equipment modules and processes and resolve related issues.
- Resolve yield and reliability issues through process development.
- Stay engaged with vendors on strategic process/hardware evaluations.
- Work closely with manufacturing Fab and understand manufacturing issues.
- Engage PI groups to understand implications of inline/probe/parametric data and find opportunities for process improvement.
- Design multi level SWRs using DOE concept and develop framework for process changes and conversion.
- Initiate and manage experiments to widen process margins as well as to test out manufacturability of next node solutions
- effective interaction with manufacturing on such areas.
- Incorporate best known manufacturing methods into early development phase of upcoming nodes.
- Provide documentations for new technology transfer.
- Interact positively with the group pilot line vendors integration manufacturing management to contribute to key improvement projects.
- Accountable for building and driving technical strategy for Diffusion Process area (Gate High k oxidation thermal/plasma treatment annealing etc) to meet DRAM and high performance CMOS technology scaling targets.
- Be a strong collaborator across other development teams including PI (Process Integration) PE (Product Engineering) QA (Quality Assurance) and device reliability and global quality teams.
- Collaborate effectively with equipment and chemical vendors to ensure alignment and build positive relationships for long term success of company.
- Be a mentor and role model for team members to build technical and leadership talent pipeline for area.
- Be accountable for key Technology Development business processes related to strategy and execution such as Roadmap Development and cost reduction and technology transfer support to HVM.
- Provide advice to senior and executive technical management related to strategic decisions for the Diffusion area.
- Make decisions that significantly impact the achievement of area functional goals product and process equipment strategy.

スキル・資格

■Requirements:

- Bachelor's/Master's/PhD in chemical/material/mechanical/electronics/physics with min 5 years working experience in Semiconductor or Electronic or Chemical Process
- Good Understanding of the Process concepts and how the hardware and process interact with each other.
- Strong desire and creativity to explore process and control improvements.
- Strong collaborator with the ability to clearly communicate with content experts · written and oral (via conference calls)
- Proven ability to set measure and achieve short and long term goals.
- Demonstrated success in leading across functional (R D Manufacturing) teams (preferably WW) in defining and solving process problems.

会社説明

ご紹介時にご案内いたします