



PR/157968 | Signal / Power Integrity Engineer

募集職種

人材紹介会社

ジェイエイシーリクルートメントマレーシア

求人ID

1512317

業種

電気・電子・半導体

雇用形態

正社員

勤務地

マレーシア

給与

経験考慮の上、応相談

更新日

2025年03月19日 08:01

応募必要条件

職務経験

3年以上

キャリアレベル

中途経験者レベル

英語レベル

ビジネス会話レベル

日本語レベル

ビジネス会話レベル

最終学歴

短大卒：準学士号

現在のビザ

日本での就労許可は必要ありません

募集要項

Signal/ Power Integrity Engineer

Rapidly expanding IC design platform company specialized in IP, SOC and ASIC design services hiring talented and experienced SIPI Engineer. You will part of well-trained engineers/designers who support customers in High-Performance Computing, AI, 5G & Network and Automotive industries using advanced technology.

Job Descriptions:

- Responsible for signal integrity (SI) and power integrity (PI) analysis and optimization.
- Perform packaging / system-level SIPI simulation analysis, extract parameters such as insertion loss, return loss, TDR, eye diagram, DC IR, PDN, AC ripple etc., assist packaging designers in optimizing design details, guide layout implementation, and write simulation reports.
- Participate in ball-map layout planning and packaging design reviews.
- Assist internal design teams and external clients in establishing reasonable SIPI standards.
- Provide SIPI simulation guidance for IP product development.

Job Requirements:

- Bachelor's degree or above in Electronics or related fields, with over 5 years of SIPI work experience.
- Proficient in theoretical knowledge of signal integrity and power integrity.

- Proficient in using SIPI-related design tools such as HFSS, SIWAVE, ADS, PowerSI, HSPICE etc.
- Experience in simulation of high-speed IPs such as DDR and SerDes.
- Strong team spirit, willing to communicate and share knowledge.

Interested to explore this exciting opportunity? Apply below:

会社説明