



## PR/158020 | Senior / Staff Digital Design Engineer

## 募集職種

## 人材紹介会社

ジェイエイシーリクルートメントマレーシア

## 求人ID

1505321

## 業種

電気・電子・半導体

## 雇用形態

正社員

## 勤務地

マレーシア

## 給与

経験考慮の上、応相談

## 更新日

2024年11月19日 11:35

## 応募必要条件

## 職務経験

3年以上

## キャリアレベル

中途経験者レベル

## 英語レベル

ビジネス会話レベル

## 日本語レベル

ビジネス会話レベル

## 最終学歴

短大卒：準学士号

## 現在のビザ

日本での就労許可は必要ありません

## 募集要項

## Staff Digital Design Engineer

Rapidly expanding IC design platform company specialized in IP, SOC and ASIC design services hiring talented and experienced Digital Design Engineer. You will part of well-trained engineers/designers who support customers in High-Performance Computing, AI, 5G & Network and Automotive industries using advanced technology.

## Job Descriptions:

- Participate in high-level product specifications, microarchitecture and implementation of high-speed memory interfaces
- Perform RTL coding, LINT checking and sanity testing on implemented designs
- Work with the verification team for lab debugging
- Work with the software team and/or customers to solve problems, debug and tune system performance

## Job Requirements:

- Bachelor's degree in communications, electronic engineering or computer engineering, master's degree preferred
- More than 6 years of ASIC design experience, familiar with ASIC development process
- Good Verilog HDL coding skills and EDA tools such as synthesis and timing analysis
- Familiarity with high-speed interfaces such as DDR, SerDes, PCIe is preferred
- Ability to solve customer problems and deliver results in a timely manner

- Strong organizational and communication skills.

Interested to explore this exciting opportunity? Apply below:

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会社説明