



JAC Recruitment

We are recruitment specialists around the globe

Singapore



PR/094361 | Analog Design Staff Engineer (SOC/ SerDes)

募集職種

人材紹介会社

ジェイエイシーリクルートメントシンガポール

求人ID

1502717

業種

その他（メーカー）

雇用形態

正社員

勤務地

シンガポール

給与

経験考慮の上、応相談

更新日

2024年11月12日 10:11

応募必要条件

職務経験

3年以上

キャリアレベル

中途経験者レベル

英語レベル

ビジネス会話レベル

日本語レベル

ビジネス会話レベル

最終学歴

短大卒：準学士号

現在のビザ

日本での就労許可は必要ありません

募集要項

JOB RESPONSIBILITIES

- Participate in high-level product specifications, microarchitecture and implementation of high-speed memory interfaces
- Perform RTL coding, LINT checking and sanity testing on implemented designs
- Work with the verification team for lab debugging
- Work with the software team and/or customers to solve problems, debug and tune system performance

JOB REQUIREMENTS

- Bachelor's degree in communications, electronic engineering or computer engineering, master's degree preferred

- More than 6 years (staff engineer) and 10 years (senior staff engineer) of ASIC design experience, familiar with ASIC development process
- Good Verilog HDL coding skills and EDA tools such as synthesis and timing analysis
- Familiarity with high-speed interfaces such as DDR, SerDes, PCIe is preferred
- Ability to solve customer problems and deliver results in a timely manner
- Strong organizational and communication skills where English is a must and Chinese is preferred

Interested applicants please send your resume to:

Jun Fong Khor

Email: junfong.khor@jac-recruitment.com

EA Licence: 90C3026

EA Personnel No: R1439255

We regret to inform that only shortlisted candidates will be notified.

#LI-JACSG
#countrysingapore

会社説明