

CYIENT

Circuit Design Lead engineer

募集職種

採用企業名

CYIENT株式会社

支社・支店

Cyient Japan

求人ID

1491803

業種

電気・電子・半導体

雇用形態

正社員

勤務地

神奈川県, 厚木市

給与

400万円 ~ 1200万円

更新日

2025年02月19日 03:00

応募必要条件

職務経験

6年以上

キャリアレベル

中途経験者レベル

英語レベル

日常会話レベル

日本語レベル

ネイティブ

最終学歴

大学卒 : 学士号

現在のビザ

日本での就労許可が必要です

募集要項

- Analog design expert responsible for overseeing, managing and hands-on design of the analog circuitry
- Experiences required
- Extensive industrial experience (15+ years) in the analog and mixed-signal integrated circuit design
- Solid knowledge of Analog circuit blocks: design of voltage/current references, amplifiers, comparators, filters, temperature and voltage sensors, oscillators, LDOs.
- Ensure compliance with industry standards and regulations, such as EMI/EMC and safety.
- Mandatory competencies
- Language: Fluency in English & Japanese, both written and spoken.
- Problem-solving skills: Able to solve problems and implement solutions autonomously.
- Social skills: Able to work in a global team.
- Tool skills: fluent in the use of Virtuoso schematic editor for circuit design, ADE Maestro (Assembler & Explorer) for circuit verification and knowledge of Spectre simulator. Ability to use Unix/Linux commands. Ability to use Microsoft Office software (Excel, PowerPoint, Word, Outlook). Experience using version control tools (Subversion)
- Able to do high-level system definition and derive block-level specification, based on the optimal performance trade-offs.

- Able to design various analog circuits from scratch based on a given set of specifications.
 - Able to support the verification team in debugging measurement and/or yield issues.
 - You have the drive to share your knowledge and look across teams to improve overall quality and execution.
 - Able to set layout constraints and verify layouts are meeting the desired quality.
 - Excellent critical thinking and problem-solving skills.
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スキル・資格

- Layout design experience
- Chip validation experience
- functional safety design and evaluation experience.
- Microsoft visio

Mandatory Skills : Problem Solving,Mixed-Signal IC Design,Design,Mixed Signal,Analog Circuitry,Circuit Designs

Desired Skills : Problem Solving,Mixed-Signal IC Design,Design,Mixed Signal,Analog Circuitry,Circuit Designs

Skills To Be Evaluated On : Problem Solving,Mixed-Signal IC Design,Design,Mixed Signal,Analog Circuitry,Circuit Designs

会社説明