

# CYIENT

## Senior Analog Mixed-Signal Design Engineer

### 募集職種

### 採用企業名

CYIENT株式会社

### 支社・支店

Cyient Japan

### 求人ID

1491802

### 業種

電気・電子・半導体

### 雇用形態

正社員

### 勤務地

神奈川県, 厚木市

### 給与

450万円 ~ 1200万円

### 更新日

2025年03月12日 09:00

### 応募必要条件

#### 職務経験

6年以上

#### キャリアレベル

中途経験者レベル

#### 英語レベル

日常会話レベル

#### 日本語レベル

ネイティブ

#### 最終学歴

大学卒：学士号

#### 現在のビザ

日本での就労許可が必要です

### 募集要項

- Excellent work experience in Analog / Mixed Signal Layout Design.
- Hands on experience in any or multiple critical blocks such as SERDES, PHY, HDMI, PLL, ADC, DAC, LDO, Bandgap, Ref Generators, Charge Pump, Current Mirrors, Comparator, Differential Amplifier etc.
- Excellent understanding of CMOS / Bi-CMOS / SOI / FinFet process and semiconductor devices.
- Excellent understanding of Analog Layout concepts (e.g. Matching, Electro-migration, Latch-up, Coupling, Crosstalk, IR-drop, Active and Passive parasitic devices etc.
- Basic knowledge of schematic /circuit design.
- Excellent problem-solving skills in Routing Congestion, Physical Verification in Custom Layout.
- Sound knowledge and experience for verification checks like DRC / LVS / ERC / Antenna / LPE / DFM etc.
- Knowledge of various analog layout techniques, understanding of various circuit principles as affected by Layouts such as speed, capacitance, power, noise, and area.
- Excellent hands-on experience in industry standard layout and verification tools in a Linux environment of Cadence and Siemens EDA tools (Calibre). Power user of Virtuoso -XL.
- Experience using version control tools (Subversion).
- Ability to use Unix/Linux commands and Microsoft Office software (Excel, PowerPoint, Word, Outlook).

- Excellent Leadership skills and Mentor & guide team members in execution of Layout and review their work outputs for quality and delivery Responsible for timely and quality execution of Custom Layout design.
  - Excellent communication skills and proactive at work.
  - Good to have: Knowledge of Microsoft Visio, LSI chip evaluation experience, Functional safety design and evaluation experience, Semiconductor device circuit design experience.
  - Soft Skills: High learning competency, self-motivated, and ability to work in diverse areas in a flexible and dynamic environment.
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## スキル・資格

Location : Hon Atsugi, Kanagawa

Position Req : 1

Mandatory Skills : Analog Layout,Design,EDA Tools,Layout Designs

Desired Skills : Analog Layout,Design,EDA Tools,Layout Designs

Skills To Be Evaluated On : Analog Layout,Design,EDA Tools,Layout Designs

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## 会社説明