



PR/158729 | Senior Engineering Manager

Job Information

Recruiter

JAC Recruitment Malaysia

Job ID

1527079

Industry

Other (Manufacturing)

Job Type

Permanent Full-time

Location

Malaysia

Salary

Negotiable, based on experience

Refreshed

April 15th, 2025 07:01

General Requirements

Minimum Experience Level

Over 3 years

Career Level

Mid Career

Minimum English Level

Business Level

Minimum Japanese Level

Business Level

Minimum Education Level

Associate Degree/Diploma

Visa Status

No permission to work in Japan required

Job Description

Senior Engineering Manager

Market leader for high-power AC to DC converters and is currently looking for Senior Engineering Manager based in Penang Malaysia.

Job Descriptions:

- Responsible for leading continuous products margin enhancement through monthly yield review to closely monitor key
 indicators and drive action needed with Product and Test Engineers.
- Regular interface with internal Test, Quality, Package, as well as subcons and steer appropriate prioritization of our efforts
- Instill robust discipline and formalized best practices within Asia Operations that will ensure we make deliver commitments without compromising yield and quality.
- Responsible for both Wafer Sort and Final Test operations and collaborate with HQ including Technology group to align on test method, limit setting, debug, FA and discrepant material disposition.
- Autonomously qualify new product derivatives through over temperature characterization. Work with various
 engineering groups at HQ to debug product/process/package issues.

Job Requirements:

- Experienced in a high-volume production environment.
- · Proven track record of problem solving and driving the resolution of poor yield and/or quality due to assembly, test and fab processes.
- Proficient in statistical data analysis and able to guide their team on issue segmentation principles.
- Experience in software like Spotfire and JMP.
- Flexible to travel as needed to several Asian factory locations (20% to 50%)
- At least 5 years of Management experience overseeing an engineering team supporting Semiconductor Product Engineering and or Assy/Test.
- More than 10 years of engineering experience as a Product Engineer.
- Knowledge of and ability to support automated test equipment, wafer sort and final testing
 Experience with CMOS circuit basics and device physics including BiCMOS is a plus.

- Experience with high voltage, high power testing is a plus.
 Bench equipment setups and bench evaluation of semiconductors experience is desired.

Company Description