

-			Malaysia	
JAC Recruitment	We are recruitme	ent specialists arou		
PR/158356 Senior FA Er	igineer			
Job Information				
Recruiter JAC Recruitment Malaysia				
Job ID 1512433				
Industry Other (Manufacturing)				
Job Type Permanent Full-time				
Location Malaysia				
Salary Negotiable, based on experience				
Refreshed April 16th, 2025 10:01				
General Requirements				
Minimum Experience Level Over 3 years				
Career Level Mid Career				
Minimum English Level Business Level				
Minimum Japanese Level Business Level				
Minimum Education Level Associate Degree/Diploma				
Visa Status No permission to work in Japan req	uired			

Job Description

Senior FA Engineer

A fast-growing organization in Penang Island is looking for experienced Failure Analysis Engineer.

Job Description:

- Perform power supply or system level failure analysis to support RMA and internal/external customer issues.
- Debugging and functionality verification of AC/DC switching power supply modules in various topologies
- Perform fault isolation and defect analysis/ characterization on power IC's to identify root causes of product failures in reliability test, production test, and/or power supply applications.
- · Provide comprehensive technical reports to customers on findings from product/power supply failure investigations.
- Advise internal and external customers regarding corrective actions based on the results of the analysis.
- Visit customers as necessary to resolve customer satisfaction issues arising from any quality problems and/or customers' perceptions
- · Liaison person resolving customers' production and field quality problems

Job Requirements:

- · Must have direct experience in customer interface on resolution of customer quality problems.
- At least 8 years of engineering experience in Switch-mode Power Supply applications, and analog, and/or mixed

signal integrated circuit devices.

- Experience in technical problem solving of switch mode power converters.
- Experience in failure analysis of power semiconductors, analog, and/or mixed signal IC devices.
- Hands-on experience in system level bench test, failure analysis, and electrical fault isolation and defect characterization tools and techniques such as the following: Oscilloscopes, Waveform Analyzer, EMI Analyzer, ESD Simulator, ATE, curve trace, micro-probing, light emission microscopy, OBIRCH, liquid crystal, CSAM, XRAY, FIB, SEM and reprocessing techniques.
- In-depth understanding of transformer design, digital and analog circuits, device physics and IC fabrication processes.
- Ability to interpret system level schematics, the IC level schematics, and IC layout of CMOS and bipolar devices is required.

Apply below:

Company Description