



## PR/158020 | Senior / Staff Digital Design Engineer

### Job Information

**Recruiter**
[JAC Recruitment Malaysia](#)
**Job ID**

1508961

**Industry**

Electronics, Semiconductor

**Job Type**

Permanent Full-time

**Location**

Malaysia

**Salary**

Negotiable, based on experience

**Refreshed**

December 11th, 2024 15:21

### General Requirements

**Minimum Experience Level**

Over 3 years

**Career Level**

Mid Career

**Minimum English Level**

Business Level

**Minimum Japanese Level**

Business Level

**Minimum Education Level**

Associate Degree/Diploma

**Visa Status**

No permission to work in Japan required

### Job Description

#### Staff Digital Design Engineer

Rapidly expanding IC design platform company specialized in IP, SOC and ASIC design services hiring talented and experienced Digital Design Engineer. You will part of well-trained engineers/designers who support customers in High-Performance Computing, AI, 5G & Network and Automotive industries using advanced technology.

**Job Descriptions:**

- Participate in high-level product specifications, microarchitecture and implementation of high-speed memory interfaces
- Perform RTL coding, LINT checking and sanity testing on implemented designs
- Work with the verification team for lab debugging
- Work with the software team and/or customers to solve problems, debug and tune system performance

**Job Requirements:**

- Bachelor's degree in communications, electronic engineering or computer engineering, master's degree preferred
- More than 6 years of ASIC design experience, familiar with ASIC development process
- Good Verilog HDL coding skills and EDA tools such as synthesis and timing analysis
- Familiarity with high-speed interfaces such as DDR, SerDes, PCIe is preferred
- Ability to solve customer problems and deliver results in a timely manner

- Strong organizational and communication skills.

Interested to explore this exciting opportunity? Apply below:

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Company Description