

CYIENT

Circuit Design Lead engineer

Job Information

Hiring Company

[Cyient K.K.](#)

Subsidiary

Cyient Japan

Job ID

1491803

Industry

Electronics, Semiconductor

Job Type

Permanent Full-time

Location

Kanagawa Prefecture, Atsugi-shi

Salary

4 million yen ~ 12 million yen

Refreshed

January 29th, 2025 10:00

General Requirements

Minimum Experience Level

Over 6 years

Career Level

Mid Career

Minimum English Level

Daily Conversation

Minimum Japanese Level

Native

Minimum Education Level

Bachelor's Degree

Visa Status

Permission to work in Japan required

Job Description

- Analog design expert responsible for overseeing, managing and hands-on design of the analog circuitry
- Experiences required
- Extensive industrial experience (15+ years) in the analog and mixed-signal integrated circuit design
- Solid knowledge of Analog circuit blocks: design of voltage/current references, amplifiers, comparators, filters, temperature and voltage sensors, oscillators, LDOs.
- Ensure compliance with industry standards and regulations, such as EMI/EMC and safety.
- Mandatory competencies
- Language: Fluency in English & Japanese, both written and spoken.
- Problem-solving skills: Able to solve problems and implement solutions autonomously.
- Social skills: Able to work in a global team.
- Tool skills: fluent in the use of Virtuoso schematic editor for circuit design, ADE Maestro (Assembler & Explorer) for circuit verification and knowledge of Spectre simulator. Ability to use Unix/Linux commands. Ability to use Microsoft Office software (Excel, PowerPoint, Word, Outlook). Experience using version control tools (Subversion)
- Able to do high-level system definition and derive block-level specification, based on the optimal performance trade-offs.

- Able to design various analog circuits from scratch based on a given set of specifications.
 - Able to support the verification team in debugging measurement and/or yield issues.
 - You have the drive to share your knowledge and look across teams to improve overall quality and execution.
 - Able to set layout constraints and verify layouts are meeting the desired quality.
 - Excellent critical thinking and problem-solving skills.
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Required Skills

- Layout design experience
- Chip validation experience
- functional safety design and evaluation experience.
- Microsoft visio

Mandatory Skills : Problem Solving,Mixed-Signal IC Design,Design,Mixed Signal,Analog Circuitry,Circuit Designs

Desired Skills : Problem Solving,Mixed-Signal IC Design,Design,Mixed Signal,Analog Circuitry,Circuit Designs

Skills To Be Evaluated On : Problem Solving,Mixed-Signal IC Design,Design,Mixed Signal,Analog Circuitry,Circuit Designs

Company Description