

CYIENT

Senior Analog Mixed-Signal Design Engineer

Job Information

Hiring Company

Cyient K.K.

Subsidiary

Cyient Japan

Job ID

1491802

Industry

Electronics, Semiconductor

Job Type

Permanent Full-time

Location

Kanagawa Prefecture, Atsugi-shi

Salary

4.5 million yen ~ 12 million yen

Refreshed

November 27th, 2024 02:00

General Requirements

Minimum Experience Level

Over 6 years

Career Level

Mid Career

Minimum English Level

Daily Conversation

Minimum Japanese Level

Native

Minimum Education Level

Bachelor's Degree

Visa Status

Permission to work in Japan required

Job Description

- Excellent work experience in Analog / Mixed Signal Layout Design.
- Hands on experience in any or multiple critical blocks such as SERDES, PHY, HDMI, PLL, ADC, DAC, LDO, Bandgap, Ref Generators, Charge Pump, Current Mirrors, Comparator, Differential Amplifier etc.
- Excellent understanding of CMOS / Bi-CMOS / SOI / FinFet process and semiconductor devices.
- Excellent understanding of Analog Layout concepts (e.g. Matching, Electro-migration, Latch-up, Coupling, Crosstalk, IR-drop, Active and Passive parasitic devices etc.
- Basic knowledge of schematic /circuit design.
- Excellent problem-solving skills in Routing Congestion, Physical Verification in Custom Layout.
- Sound knowledge and experience for verification checks like DRC / LVS / ERC / Antenna / LPE / DFM etc.
- Knowledge of various analog layout techniques, understanding of various circuit principles as affected by Layouts such as speed, capacitance, power, noise, and area.
- Excellent hands-on experience in industry standard layout and verification tools in a Linux environment of Cadence and Siemens EDA tools (Calibre). Power user of Virtuoso -XL.
- Experience using version control tools (Subversion).
- Ability to use Unix/Linux commands and Microsoft Office software (Excel, PowerPoint, Word, Outlook).

- Excellent Leadership skills and Mentor & guide team members in execution of Layout and review their work outputs for quality and delivery Responsible for timely and quality execution of Custom Layout design.
 - Excellent communication skills and proactive at work.
 - Good to have: Knowledge of Microsoft Visio, LSI chip evaluation experience, Functional safety design and evaluation experience, Semiconductor device circuit design experience.
 - Soft Skills: High learning competency, self-motivated, and ability to work in diverse areas in a flexible and dynamic environment.
-

Required Skills

Location : Hon Atsugi, Kanagawa

Position Req : 1

Mandatory Skills : Analog Layout,Design,EDA Tools,Layout Designs

Desired Skills : Analog Layout,Design,EDA Tools,Layout Designs

Skills To Be Evaluated On : Analog Layout,Design,EDA Tools,Layout Designs

Company Description