



## RF-IC Design Staff Engineer

### Job Information

#### Hiring Company

[Lumentum Japan, Inc.](#)

#### Job ID

1408509

#### Job Type

Permanent Full-time

#### Location

Tokyo - Other Areas

#### Salary

7 million yen ~ 10 million yen

#### Refreshed

July 1st, 2024 04:00

### General Requirements

#### Minimum Experience Level

Over 10 years

#### Career Level

Mid Career

#### Minimum English Level

Business Level

#### Minimum Japanese Level

Native

#### Minimum Education Level

Bachelor's Degree

#### Visa Status

Permission to work in Japan required

### Job Description

- Transiter level (SiGe & CMOS) circuit design and simulation.
- Understanding of device-level operation and physics
- Design, create and own production-ready circuit block schematics, run cadence systematic, statistical and yield simulations
- Plan for layout and supervise IC layout engineers for high-performance RF (Radio Frequency) circuit layout techniques, top level and floor plans
- Derive behavioral models for RF circuits, deliver top level verification needs
- Participate in design reviews, and create necessary owned design related documents
- Develop and drive test plans, collaborate with validation team to complete the validation phase for the owned designs
- Work with contract manufacturers manage qualification and high-volume production-testing of ICs
- Set and track IC-project, internal-DVT, qualification and production-test schedule.

### Required Skills

- MS. in Electrical Engineering, Signal Processing or Communication Systems with 3-10+ years of industry experience
- Having strong time management skills, being result oriented and committed to on-time deliverables
- In-depth understanding of high-performance, high-speed analog mixed signal circuit IC designs, calibration, equalization and signal integrity fundamentals
- Direct hands-on industrial experience in High-speed Optical Integrated circuit architectures and block designs:
  - Linear amplifiers/drivers

- Transimpedance amplifiers
  - AGC circuits
  - Linear equalizers
  - PLL/DLL
  - Low Power CMOS design (Bandgap, OpAmps, DACs...)
- In-depth knowledge of advanced calibration schemes for high-performance RF circuits.
  - Hands-on experience in full cycle of IC development and layout verifications e.g. AMS(Analog Mixed Signal) , Spectre RF, Cadence PVS, LVS, DRC, ERC
  - Direct experience with coherent optical communications links as well as electrical transceiver applications including backplane and cable communications is a plus.
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## Company Description

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